

FIG. 1

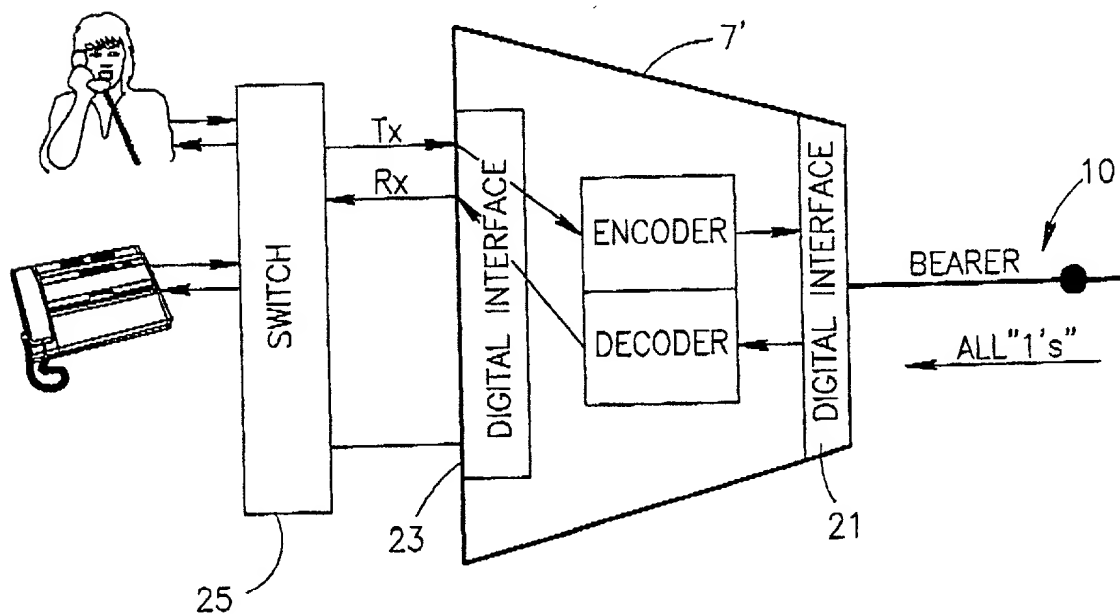


FIG. 2

FIG. 3 is a block diagram of a receiver system for a digital communication system.

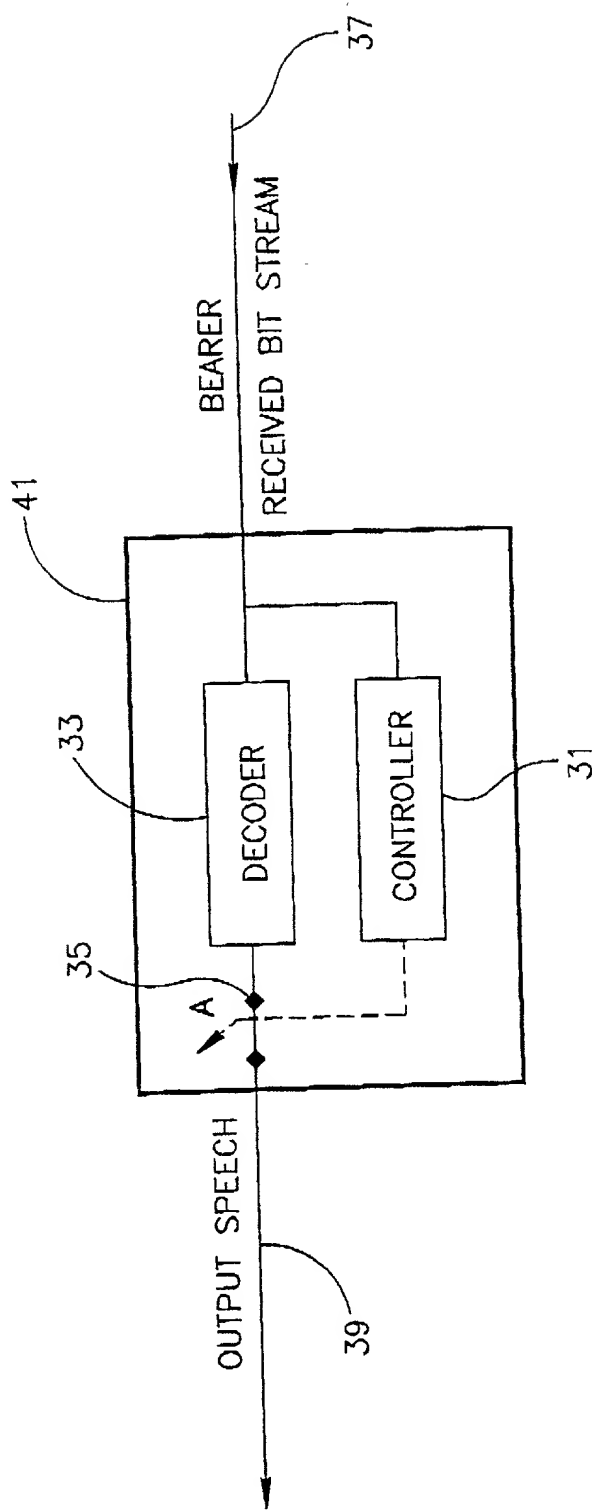


FIG.3